TOSHIBA Photocoupler GaAs Ired & Photo-Transistor

TLP624,TLP624-2,TLP624-4

Programmable Controllers

AC/DC-Input Module

Telecommunication

The TOSHIBA TLP624, -2 and -4 consist of a gallium arsenide infrared emitting diode optically coupled to a photo–transistor.

The TLP624–2 offers two isolated channels in an eight lead plastic DIP, while the TLP624–4 provides four isolated channels in a sixteen plastic DIP $\,$

- Collector-emitter voltage: 55V min.
- Current transfer ratio

	Curre	Marking		
Classi-	Ta =	25°C	Ta=-25~75°C	of
fication	I _F =1mA	I _F =0.5mA	I _F =1mA	classi-
	V _{CE} =0.5V	V _{CE} =1.5V	V _{CE} =0.5V	fication
Rank BV	200%	100%	100%	BV
Standard	100%	50%	50%	BV,blank

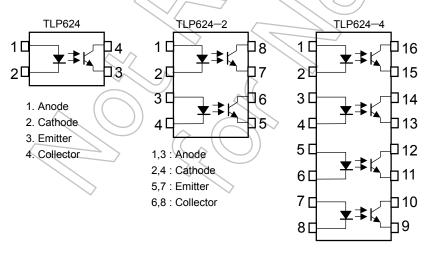
- Isolation voltage: 5000V_{rms} min.
- UL recognized: UL1577, file No.E67349
- BSI approved: BS EN60065: 2002 Certificate No.7426

BS EN60950-1: 2002 Certificate No.7427

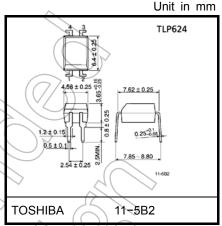
 Note: Application type name for certification test, please use standard product type name, i.e.

> TLP624(BV): TLP624 TLP624-2(BV): TLP624-2

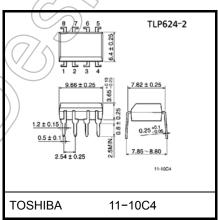
Pin Configurations (top view)



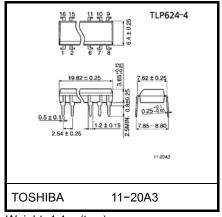
1,3,5,7: Anode 2,4,6,8: Cathode 9,11,13,15: Emitter 10,12,14,16: Collector



Weight: 0.26 g (typ.)



Weight: 0.54 g (typ.)



Weight: 1.1 g (typ.)



Absolute Maximum Ratings (Ta = 25°C)

				Ra	ting	
	Characteristic		Symbol	TLP624	TLP624-2 TLP624-4	Unit
	Forward current		ΙF	60	50	mA
	Forward current detating		ΔI _F / °C	–0.7(Ta ≥ 39°C)	–0.5(Ta ≥ 25°C)	mA / °C
	Pulse forward current		l _{FP}	1(100µs, pu	lse, 100pps)	Α
LED	Power dissipation(1 Circuit)		PD	100	70	mW
П	Power dissipation derating (Ta ≥ 25°C, 1 Circuit)		ΔP _D / °C	-1.0	-0.7	mW / °C
	Reverse voltage		V _R		5	V
	Junction temperature		Tj	12	25	°C
	Collector-emitter voltage		V _{CEO}	5	5	V
	Emitter-collector voltage		V _{ECO}		7	V
tor	Collector current		Ic	5	0	mA
Detector	Collector power dissipation(1 circuit)		Pc	150	100	mW
	Collector power dissipation derating (Ta ≥ 25°C, 1 Circuit)		ΔPc /°C	-1.5	1.0	mW / °C
	Junction temperature		Ťj	(7)	25	°C
Stor	age temperature range		→ T _{stg}	-55~125		°C
Ope	rating temperature range		Popr	-55~100		°C
Lead	Lead soldering temperature		T _{sol}	260((10s)	°C
Tota	ll package power dissipation(1 Circuit)		/PT	250	150	mW
Tota (Ta	ll package power dissipation derating ≥ 25°C, 1 Circuit)	→	ΔP _T /°C	-2.5	-1.5	mW / °C
Isola	ation voltage	(Note 1)	BVS	5000(AC, 1m	in., RH≤60%)	Vrms

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

(Note 1) Device considered a two terminal device: LED side pins shorted together, and detector side pins shorted together.

Recommended Operating Conditions

Characteristic	Symbol	Min.	Тур.	Max.	Unit
Supply voltage	V _{CC}	_	5	24	V
Forward current	\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\\	_	1.6	20	mA
Collector current	IC	_	1	10	mA
Operating temperature	T _{opr}	-25	_	75	°C

Note: Recommended operating conditions are given as a design guideline to obtain expected performance of the device. Additionally, each item is an independent guideline respectively. In developing designs using this product, please confirm specified characteristics shown in this document.



Individual Electrical Characteristics (Ta = 25°C)

	Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
	Forward voltage	V _F	I _F = 10mA	1.0	1.15	1.3	V
LED	Reverse current	I _R	V _R = 5V	_	_	10	μΑ
	Capacitance	C _T	V = 0, f = 1MHz	_ <	30	_	pF
	Collector–emitter breakdown voltage	V _{(BR)CEO}	I _C = 0.5mA	55		7	V
tor	Emitter-collector breakdown voltage	V _{(BR)ECO}	I _E = 0.1mA	70	$\geq / ($	_	٧
Detector	Collector dark current	lana	V _{CE} = 24V	/ [V])10	100	nA
	Collector dark current	ICEO	V _{CE} = 24V, Ta = 85°C	1	2	50	μΑ
	Capacitance collector to emitter	C _{CE}	V=0 , f=1MHz		12		pF

Coupled Electrical Characteristics (Ta = 25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Current transfer ratio	I _C / I _F	I _F = 1mA, V _{GE} = 0.5V Rank BV	100	7 =	1200 1200	%
Low input CTR	I _C / I _F (low)	I _F = 0.5mA, V _{CE} = 1.5V Rank BV	100) –	_	%
Collector-emitter	V _{CE}	I _C = 0.5mA, I _F = 1mA)_	_	0.4	
saturation voltage	(sat)	I _C = 1mA, I _F = 1mA Rank BV))-	0.2	0.4	V

Coupled Electrical Characteristics (Ta = -25°C~75°C)

Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Current transfer ratio	J _{IC} /I _F	I _F = 1mA, V _{CE} = 0.5V	50		%	
	Rank BV	100	-	_		
Low input CTR	Ic/I _F	I _F = 0.5mA, V _{CE} = 1.5V		50	_	%
	(low)	Rank BV	1	100		70



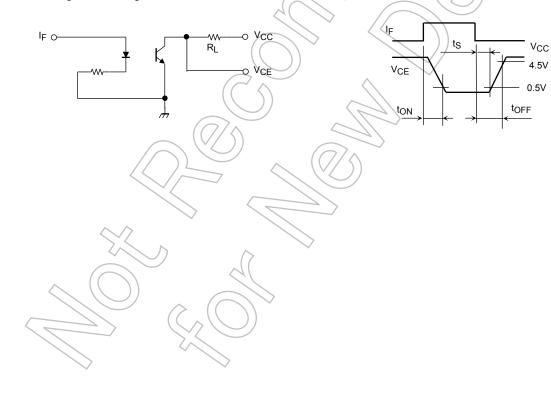
Isolation Characteristics (Ta = 25°C)

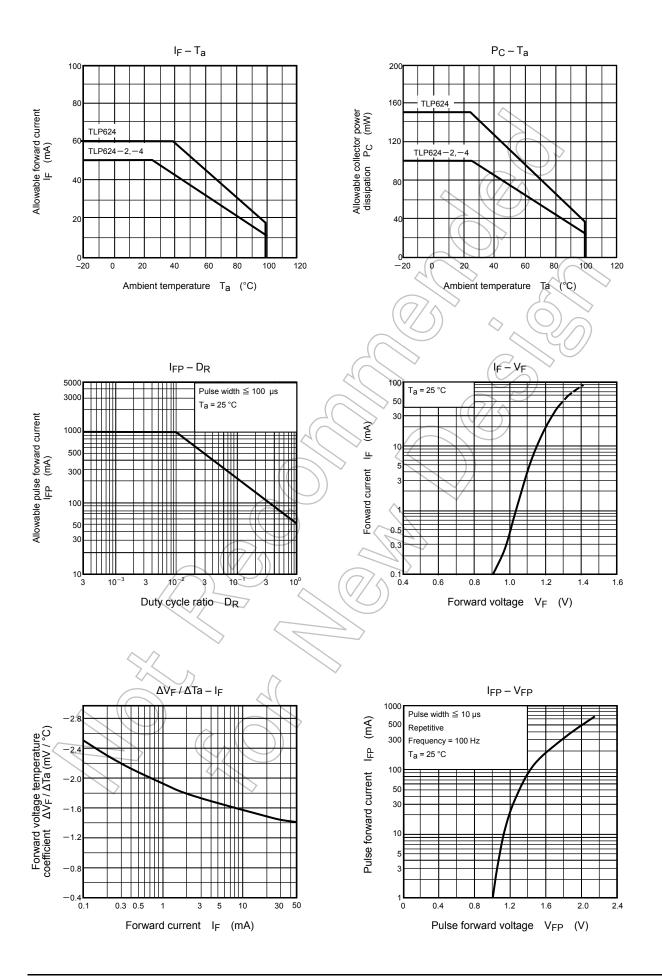
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Capacitance input to output	CS	V _S = 0, f = 1MHz	_	0.8	_	pF
Isolation resistance	R _S	V _S = 500V	5×10 ¹⁰	10 ¹⁴	_	Ω
Isolation voltage		AC, 1minute	5000	<u> </u>	_	Vrms
	BV_S	AC, 1second, in oil	_	10000	_	VIIIIS
		DC, 1 minute, in oil	_	10000)~	Vdc

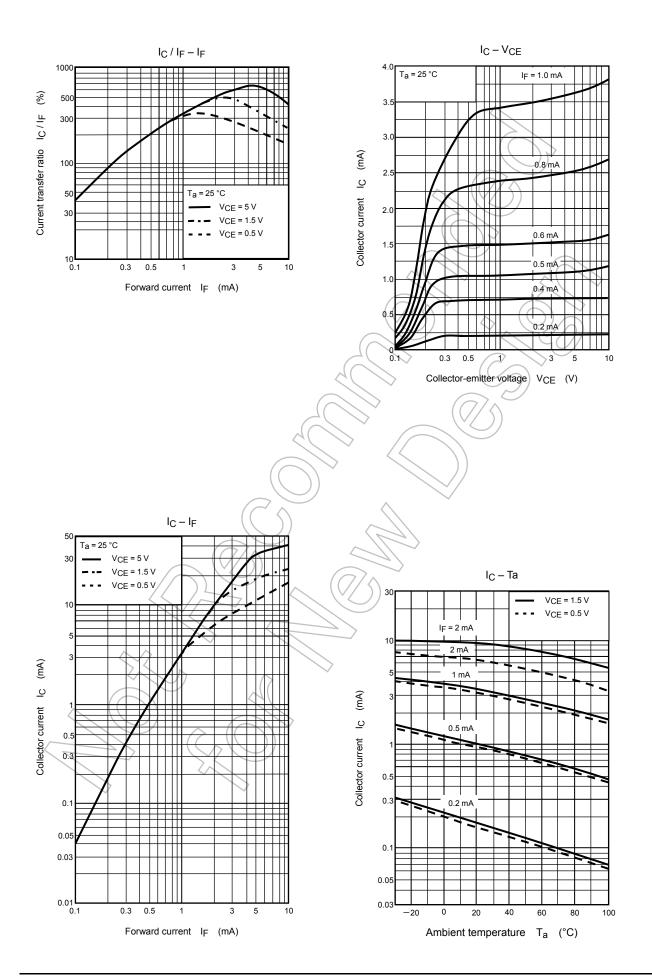
Switching Characteristics (Ta = 25°C)

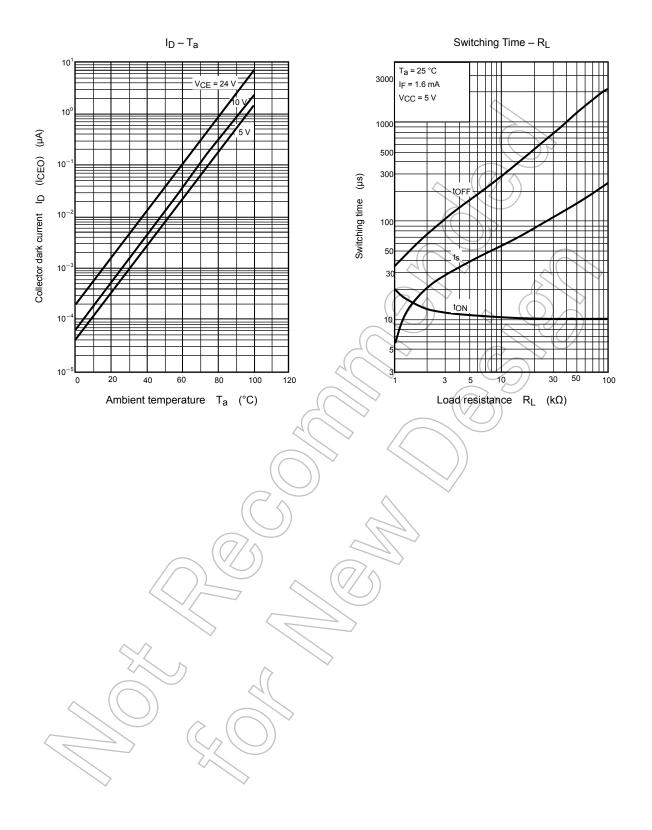
Characteristic	Symbol	Test Condition	Min.	Тур.	Max.	Unit
Rise time	t _r)	8		
Fall time	t _f	$V_{CC} = 10V$, $I_C = 2mA$ $R_L = 100\Omega$	_	8	47	110
Turn-on time	t _{on}		> _	10	7-//	μs
Turn-off time	t _{off}		_<	8	7 / 5)
Turn-on time	t _{ON}		_	10	70/	
Storage time	ts	$R_L = 4.7 \text{ k}\Omega \text{ (Fig.1)}$ $V_{CC} = 5 \text{ V, I}_F = 1.6 \text{mA}$	-((50	⁷ –	μs
Turn-off time	T _{OFF}			300	_	

Fig. 1 Switching time test circuit









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